

FIG. 4

AND, BASED ON EXECUTION  
OF THE INSTRUCTION,  
ARRANGE FOR A  
SUBSEQUENT INSTRUCTION  
TO NOT BE PROVIDED  
FROM THE DIRECT MEMORY  
ACCESS UNIT TO THE  
PROCESSING ELEMENT

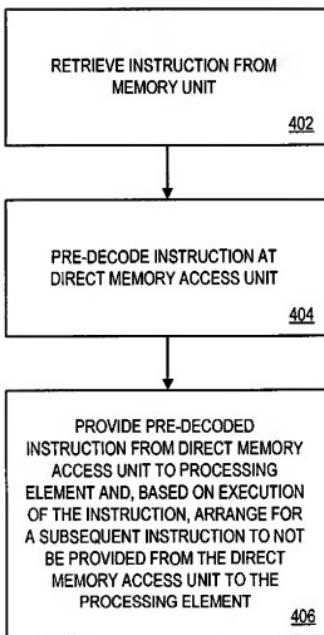


FIG. 4